



Universidade de Brasília

# Algebraic Modeling of Continuous Time Systems<sup>1</sup>

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<sup>1</sup>Some slides kindly provided by Prof. Ingo Sander from KTH.



FORÇA AEREA BRASILEIRA

4100

DANIEL



CYBER

PHYSICAL



SECURITY

ALGORITHMS

ADAPTABILITY

CYBER

MAN IN THE LOOP!

DYNAMICS

PHYSICAL

SAFETY

REPEATABILITY

4100

FORÇA AEREA BRASILEIRA

**"YOU WILL NEVER STRIKE  
OIL BY DRILLING THROUGH  
THE MAP"  
SOLOMON GOLOMB**



*"Many predictive properties that we assert about systems (determinism, timeliness, reliability, safety, ...) are in fact not properties of an implemented system, but rather properties of a model of the system."*<sup>2</sup>

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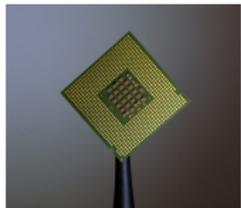
<sup>2</sup>Prof. Hermann Kopetz apud Cyber-Physical Systems: A Rehash or a New Intellectual Challenge?; Edward Lee; Design Automation Conference (DAC); 2013.

*"Many predictive properties that we assert about systems (determinism, timeliness, reliability, safety, ...) are in fact not properties of an implemented system, but rather properties of a model of the system."<sup>2</sup>*

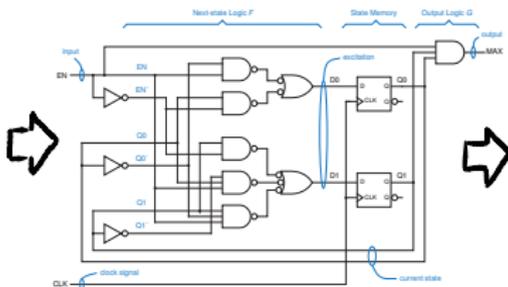
**IT'S ALL ABOUT MODELS!**

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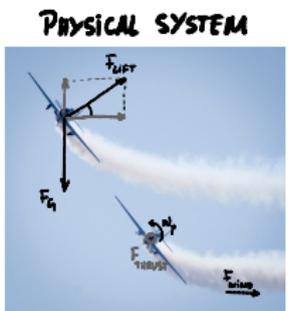
<sup>2</sup>Prof. Hermann Kopetz apud Cyber-Physical Systems: A Rehash or a New Intellectual Challenge?; Edward Lee; Design Automation Conference (DAC); 2013.



ASIC



SYNCHRONOUS DIGITAL LOGIC



PHYSICAL SYSTEM

$$u(t) = K_p e(t) + \frac{K_p}{T_i} \int_0^t e(t) dt + K_p T_d \frac{de(t)}{dt}$$

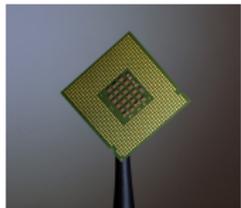
DIFFERENTIAL EQUATIONS

## SINGLE-THREADED IMPERATIVE PROGRAMS

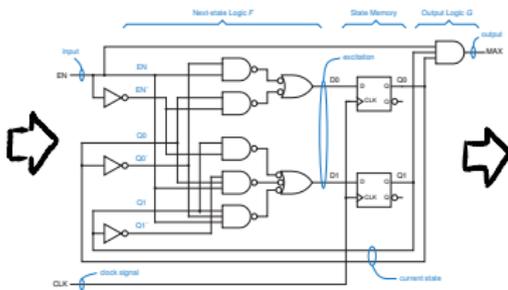
```

1 #include <msp430.h>
2
3 int main(void) {
4     WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
5
6     //Configure output ports
7     P1DIR |= BIT0; // P1.0 = LED
8
9     //Configure clock system
10    UCSCTL0 = 0x0000; // Set lowest possible DCOx, MDCx
11    // These are controlled by FLL
12    UCSCTL1 = DCORSEL_5; // Select DCO range to 16MHz
13    UCSCTL2 |= 243; // (N+1) * 32.768kHz = 16MHz
14    UCSCTL3 = SELREF_2; // Set DCO FLL reference = REFO
15    UCSCTL4 = SELA_0 | SELS_4 | SELM_3; // Set ACLR = XT1,
16    // SMCLK = DCOCLKDIV, MCLK = DCOCLK
17    UCSCTL5 = DIVPA_0 | DIVA_0 | DIVS_3 | DIVM_0; // Set SMCLK divider
18    _delay_cycles(250000);
19
20    //Configure timer AB
21    TABCTL = TASSEL_1 | MC_1 | TAQR; // SMCLK = 32768Hz, Up Mode
22    TABCCR0 = 32767; // 1 second period
23
24    while(1){
25        P1OUT ^= BIT0;
26
27        while((TABCTL & TAIFG) == 0){
28        }
29        TABCTL &= ~(TAIFG);
30    }
31
32    return 0;
33 }
    
```

# MODEL FOR CIRCUITS

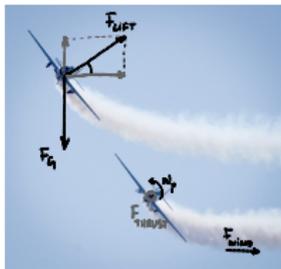


ASIC



SYNCHRONOUS DIGITAL LOGIC

## PHYSICAL SYSTEM



$$u(t) = K_p e(t) + \frac{K_p}{T_i} \int_0^t e(t) dt + K_p T_d \frac{de(t)}{dt}$$

DIFFERENTIAL EQUATIONS

## SINGLE-THREADED IMPERATIVE PROGRAMS

```

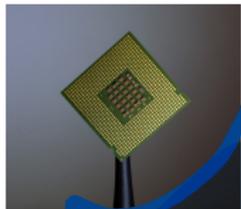
1 #include <msp430.h>
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26
27        while((TABCTL & TAIFG) == 0){
28        }
29        TABCTL &= ~(TAIFG);
30    }
31
32    return 0;
33 }
    
```

## MODEL FOR ALGORITHMS/ PROCESSES

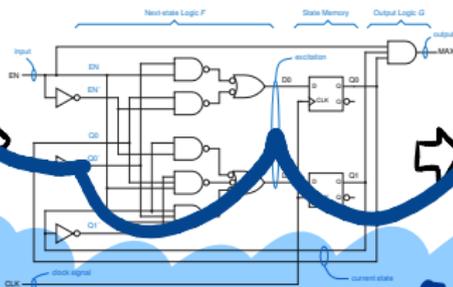
## MODEL FOR DYNAMICS

MODEL FOR CIRCUITS ↴

SINGLE-THREADED IMPERATIVE PROGRAMS



ASIC

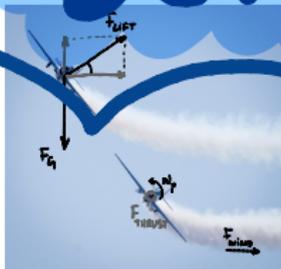


SYNCHRONOUS DIGITAL

```

1 #include <msp430.h>
2
3 int main(void) {
4     WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
5
6     //Configure output ports
7     P1DIR |= BIT0; // LED
8
9     //Configure clock system
10    UCSCCTL0 = 0x0000; // Set lowest possible DCOs, MDCs
11    // These are controlled by FLL
12    DCOCTL = 0x0000; // Select DCO range to 16MHz
13    // (N+1) * 32.768 = 16MHz
14    UCSCCTL3 = SELREF_2; // Set DCO FLL reference to REF0
15    UCSCCTL4 = SELA_4 | SELS_4 | SELM_3; // Set ACLK = XT1
16    // SMCLK = DCOCLKDIV, MCLK = DCOCLK
17    UCSCCTL5 = DIVPA_0 | DIVA_0 | DIVS_3 | DIVM_0; // Set SMCLK divider
18    //_delay_cycles(250000);
19
20    //Configure timer AB
21    TARBCTL = TASSEL_1 | MC_1 | TAPR_3; // SMCLK / 32768Hz, Up Mode
22    TCCR0 = 32767; // TCCR0 = 32767, 1 period
23
24    led = &P1OUT;
25    P1DIR |= BIT0;
26
27    while (TA0R0 == 0) {
28        TARBCTL &= ~(TAIFG);
29    }
30
31    return 0;
32 }
    
```

# DETERMINISM



PHYSICAL SYSTEM

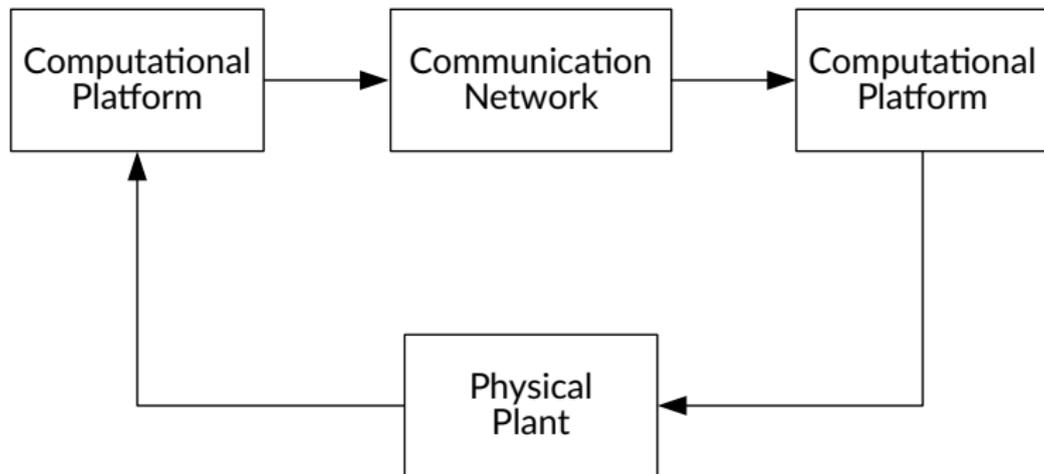
MODEL FOR ALGORITHMIC PROCESSES

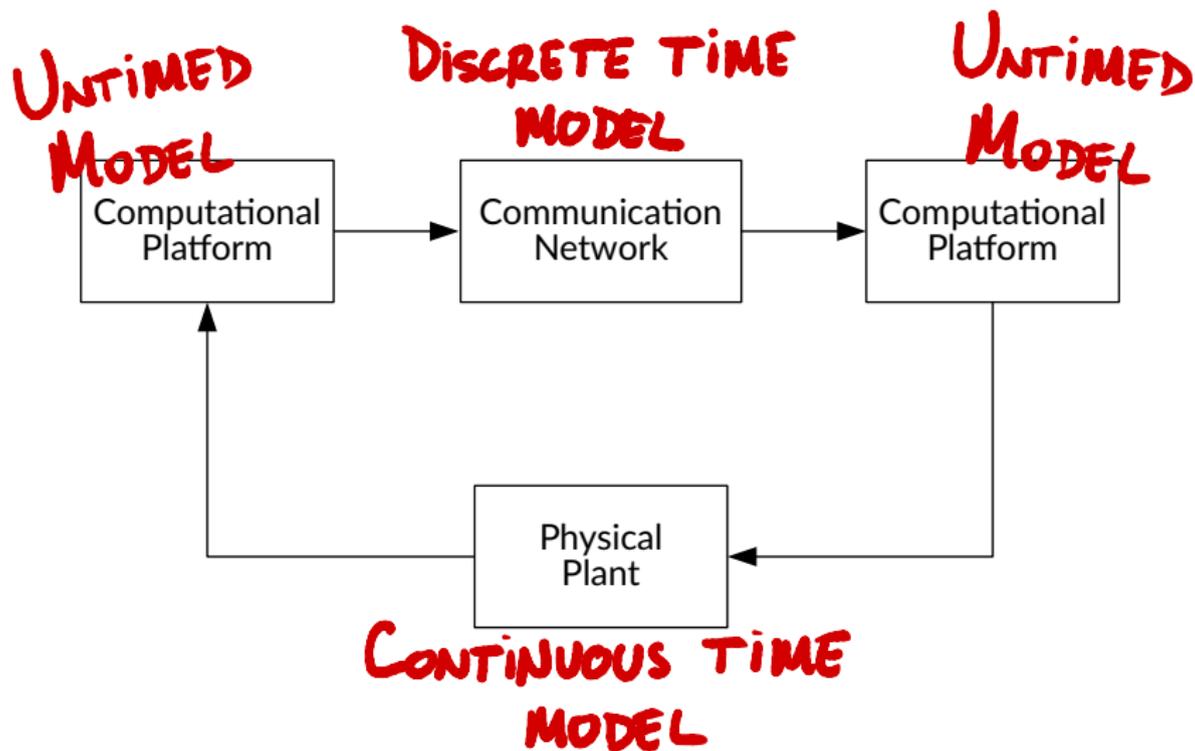


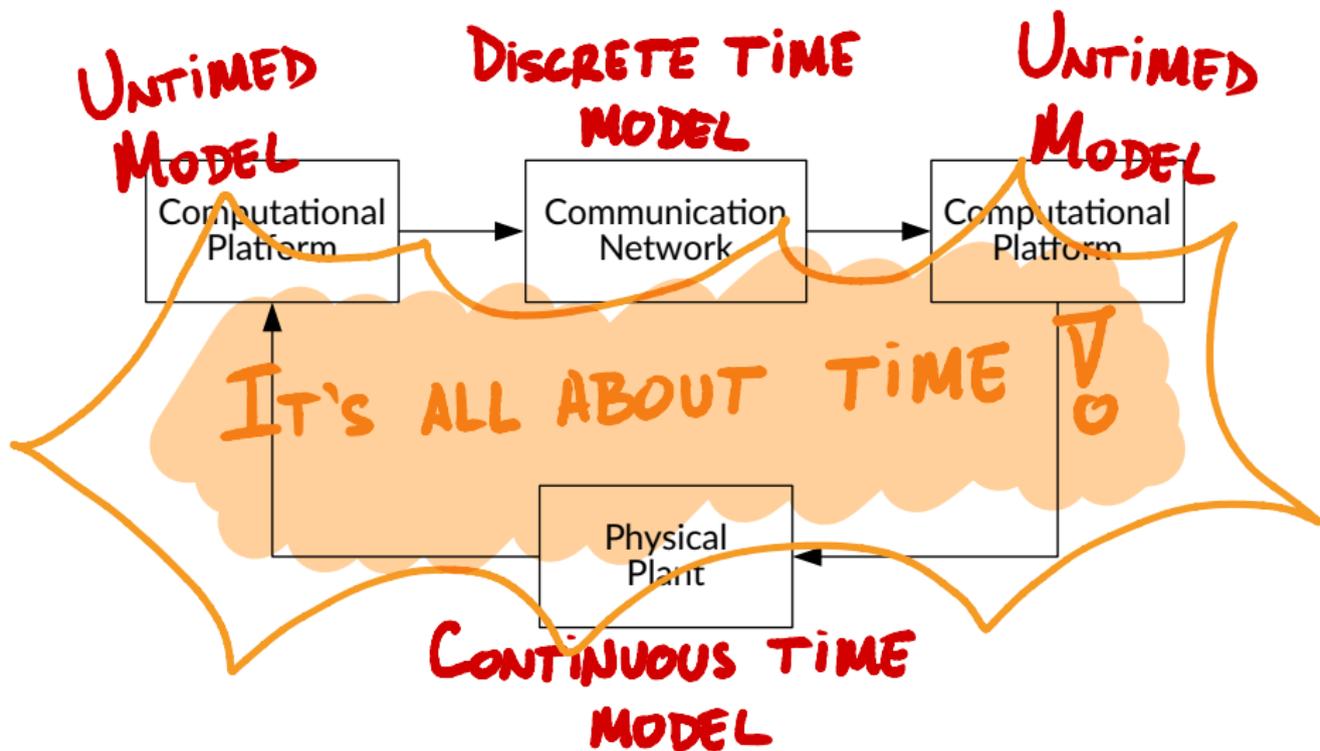
$$u(t) = K_p e(t) + \frac{K_p}{T_i} \int_0^t e(t) dt + K_p T_d \frac{de(t)}{dt}$$

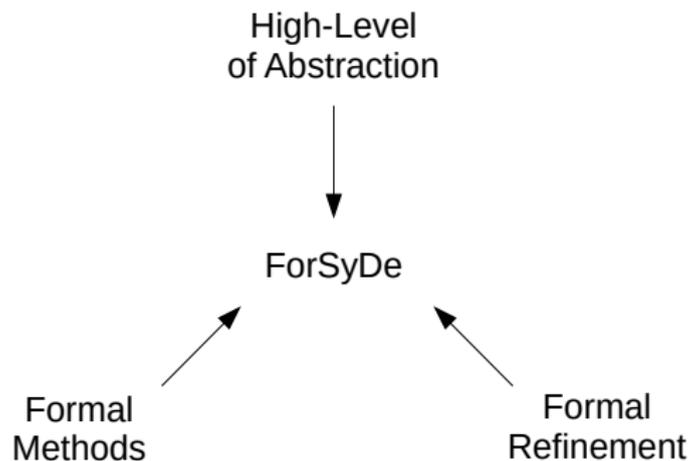
DIFFERENTIAL EQUATIONS

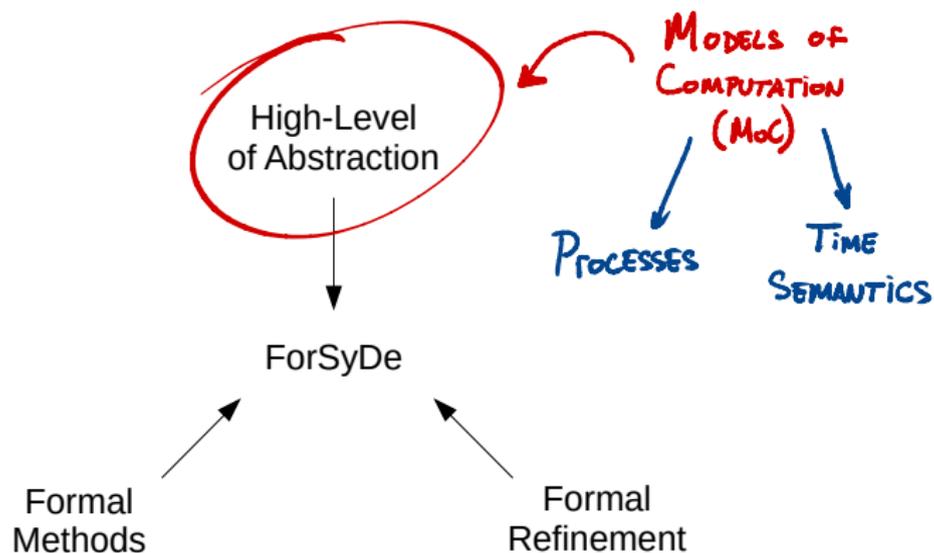
MODEL FOR DYNAMICS

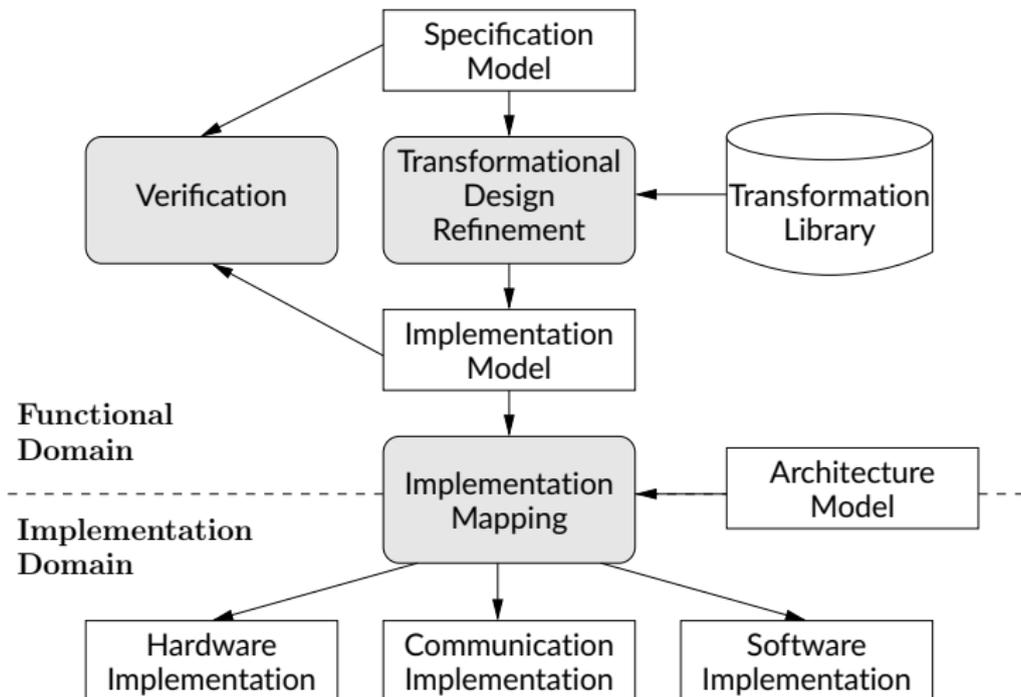


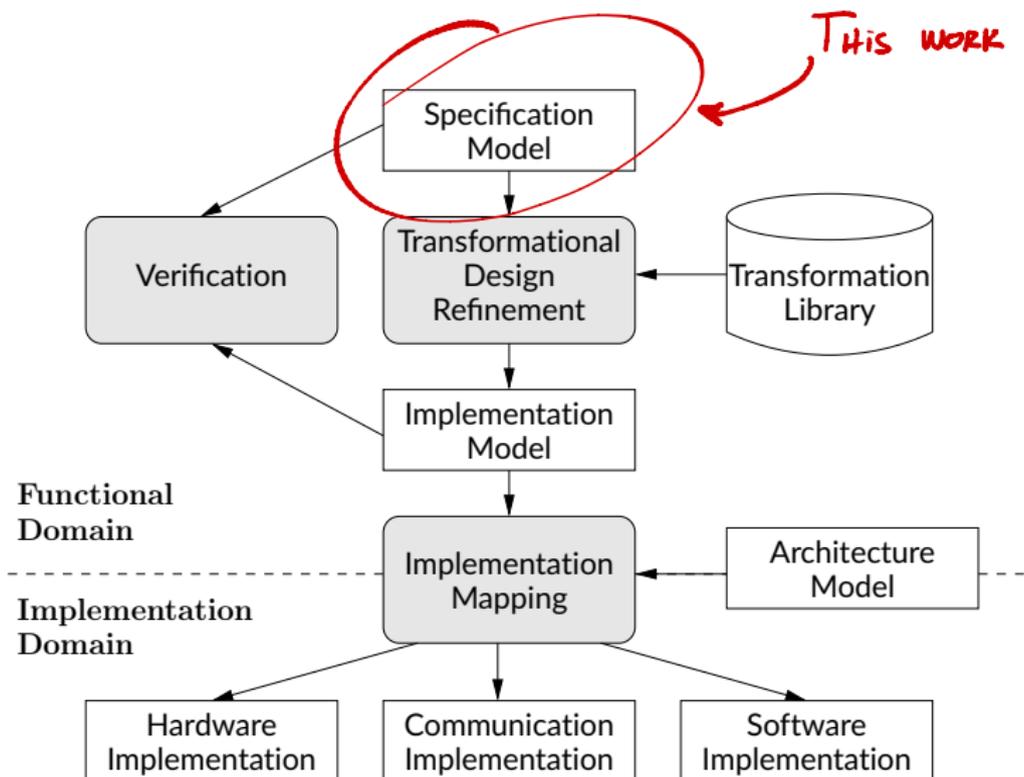


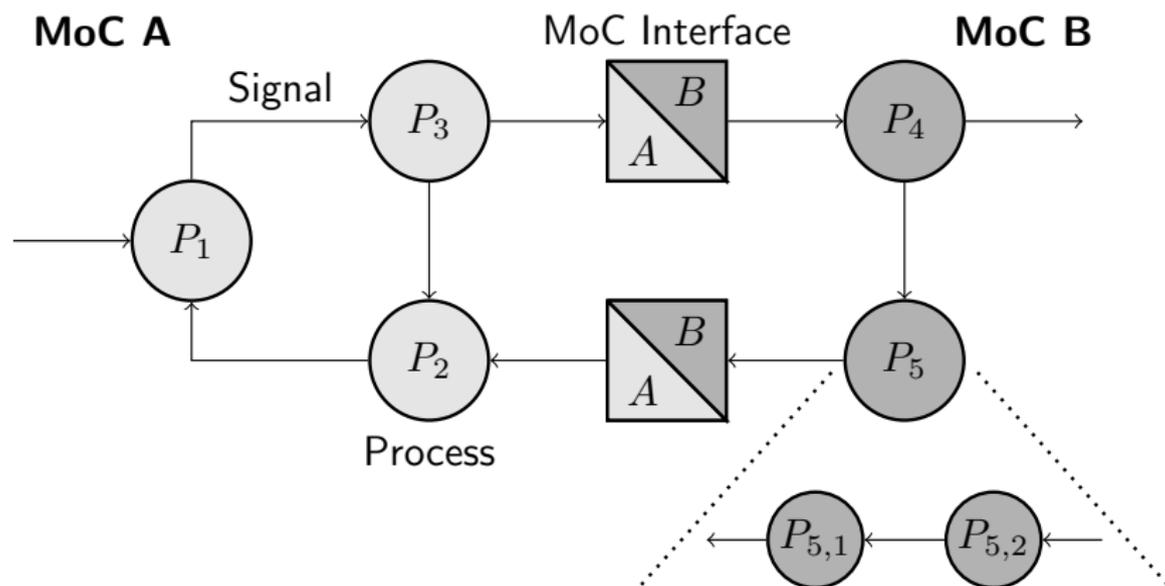






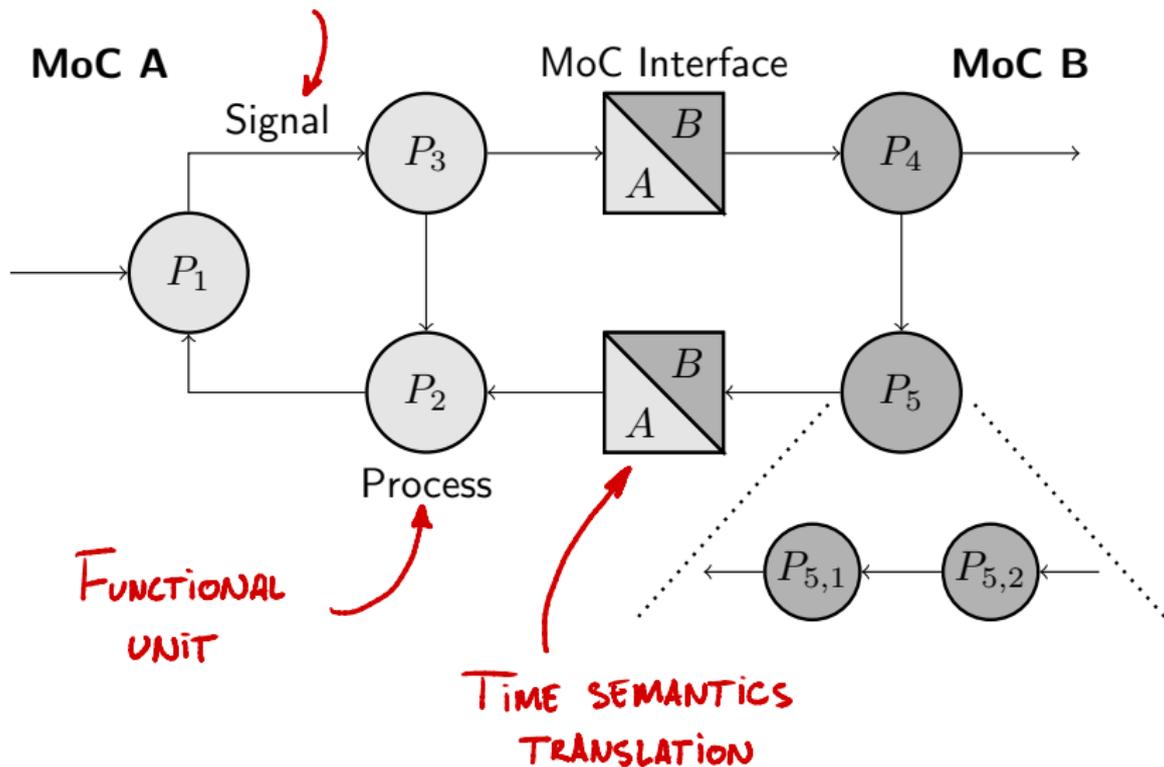


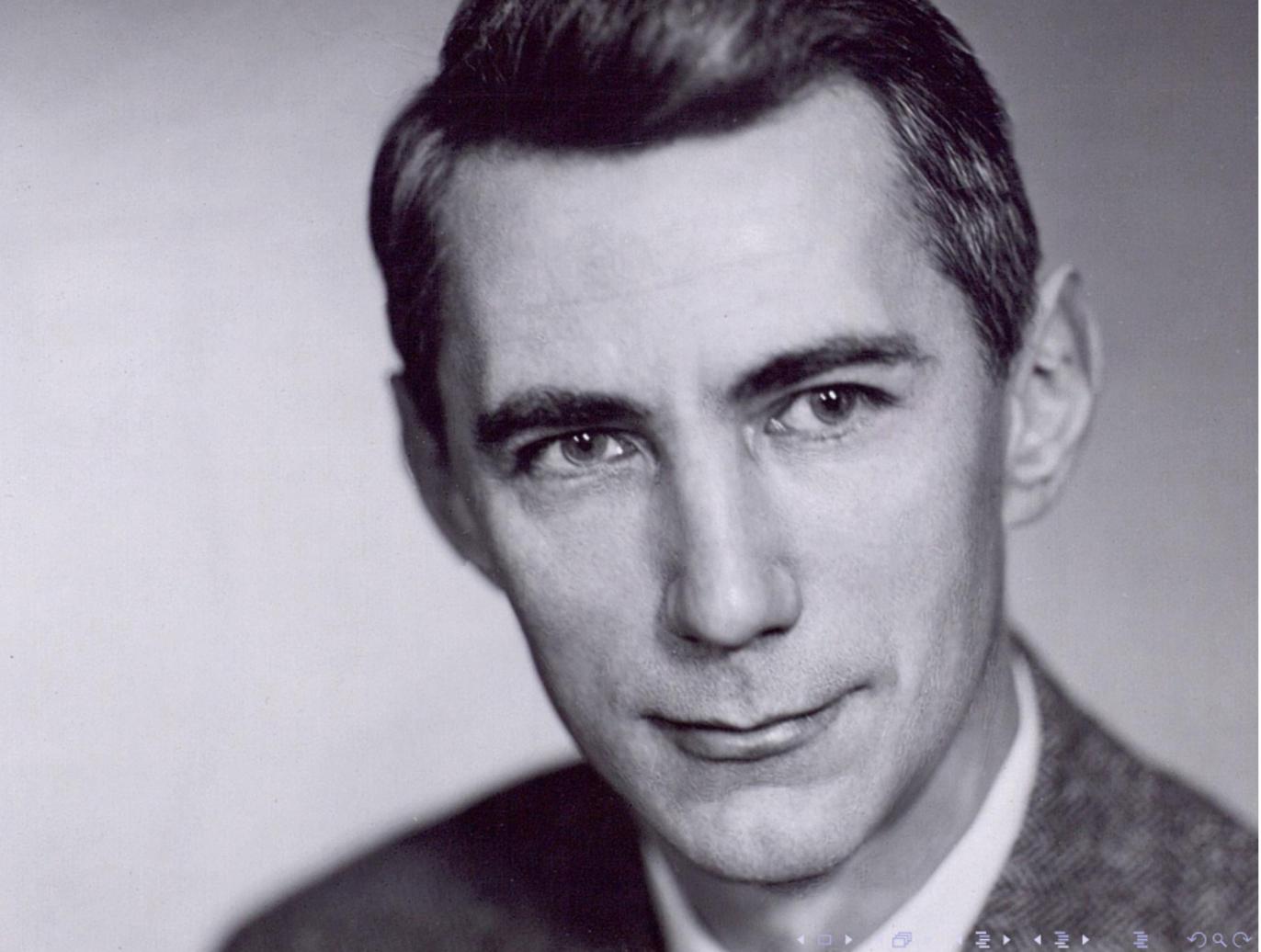


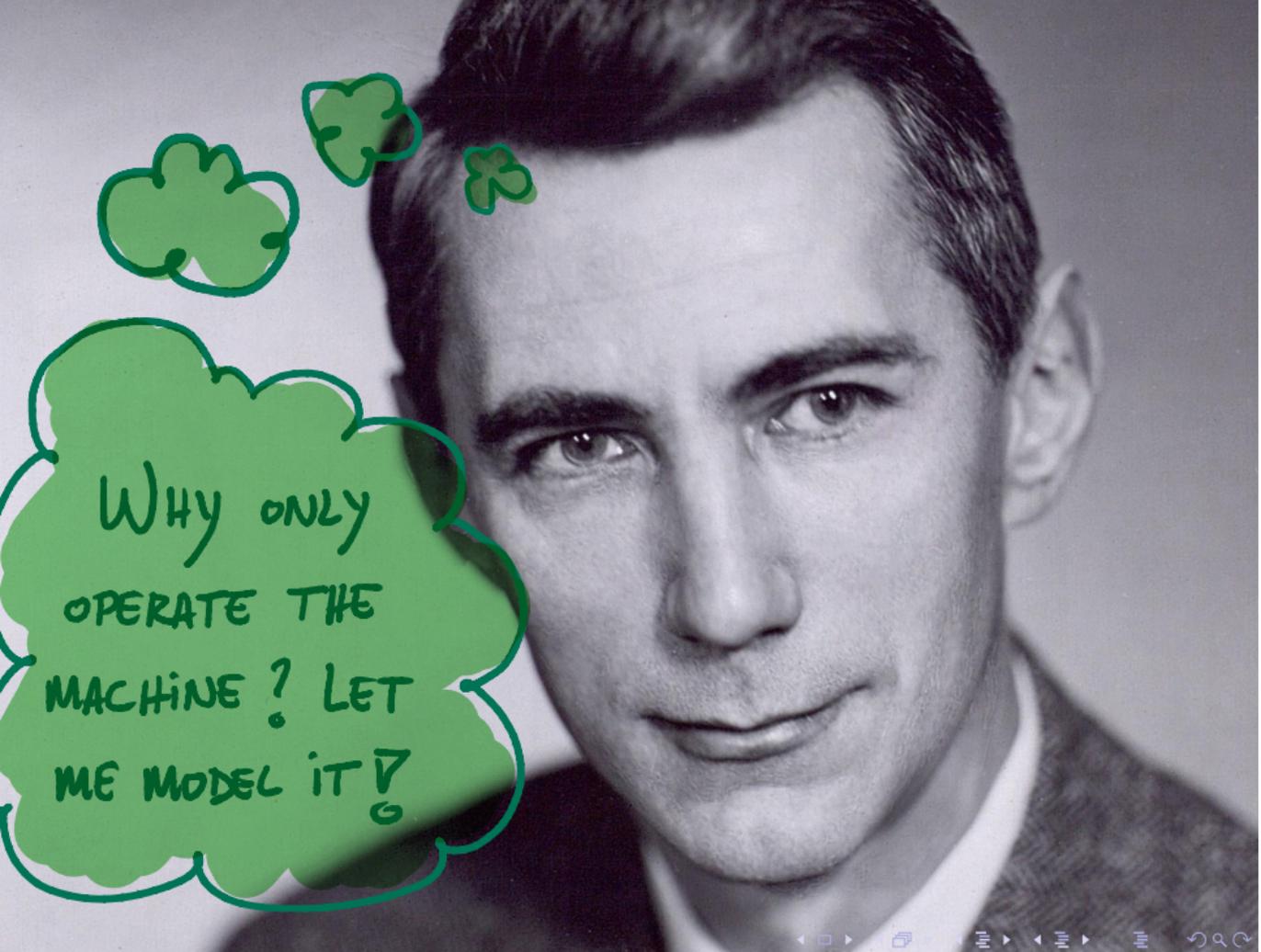


COMMUNICATION / SYNCHRONIZATION  
BETWEEN UNITS

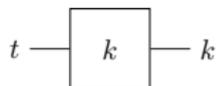
TIME SEMANTICS GO HERE.







Why ONLY  
OPERATE THE  
MACHINE? LET  
ME MODEL IT!



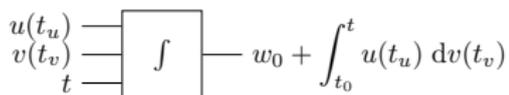
A constant unit



An adder unit



A multiplier unit

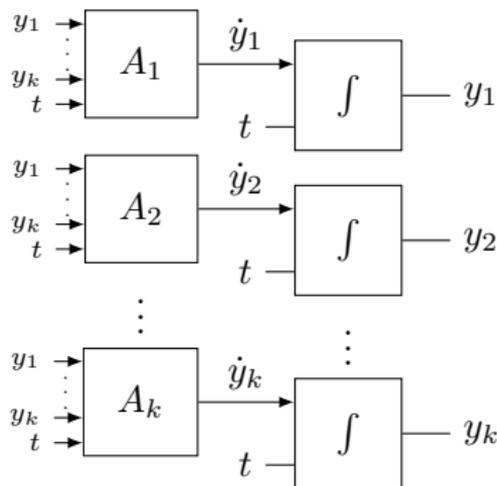


An integrator unit

**BASIC UNITS**

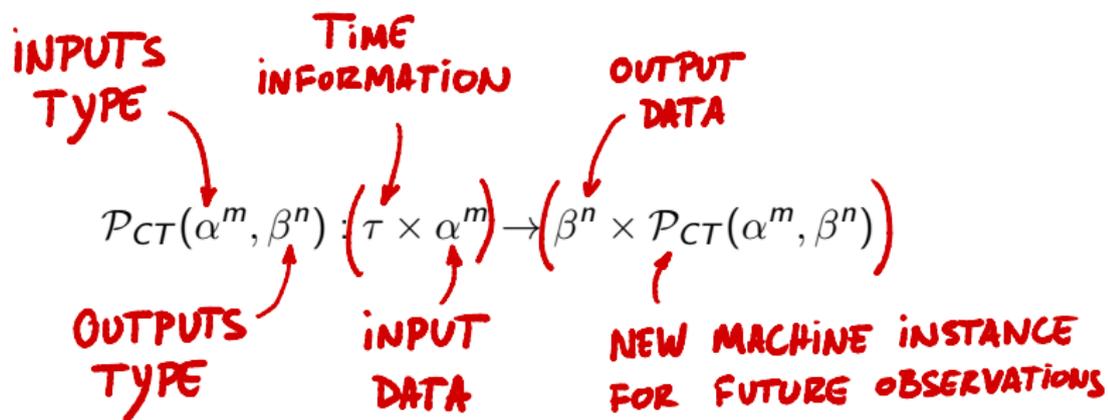
(AXIOMS)

→ **GENERAL PURPOSE ANALOG COMPUTER (GPAC)**



**COMPOSITION RULES**

$$\mathcal{P}_{CT}(\alpha^m, \beta^n) : \tau \times \alpha^m \rightarrow \beta^n \times \mathcal{P}_{CT}(\alpha^m, \beta^n)$$



"NEXT STATE"  
FUNCTION

$$\mathcal{P}_{CT}(\alpha^m, \beta^n) : \tau \times (\alpha^m \rightarrow \beta^n) \times \mathcal{P}_{CT}(\alpha^m, \beta^n)$$

$$\mathit{const}_{CT} : \mathcal{P}_{CT}(\alpha, \beta)$$

$$\mathit{const}_{CT}^k = (t, a) \mapsto (k, \mathit{const}_{CT}^k)$$

$$\mathit{add}_{CT} : \mathcal{P}_{CT}(\alpha \times \alpha, \alpha)$$

$$\mathit{add}_{CT} = (t, a, b) \mapsto (a + b, \mathit{add}_{CT})$$

$$\mathit{mult}_{CT} : \mathcal{P}_{CT}(\alpha \times \alpha, \alpha)$$

$$\mathit{mult}_{CT} = (t, a, b) \mapsto (ab, \mathit{mult}_{CT})$$

$$\triangleright : \mathcal{P}_{CT}(\alpha, \beta) \times \mathcal{P}_{CT}(\beta, \gamma) \rightarrow \mathcal{P}_{CT}(\alpha, \gamma)$$

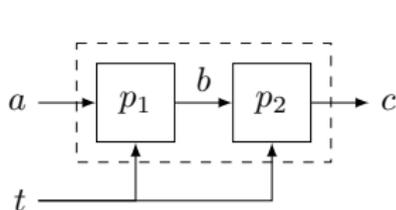
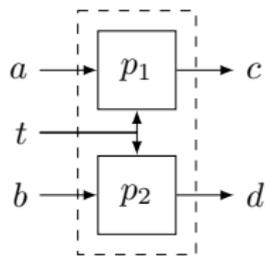
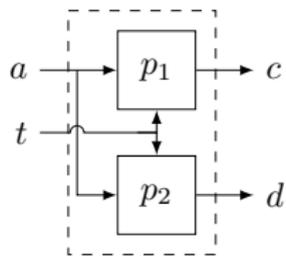
$$p_1 \triangleright p_2 = (t, a) \mapsto (c, p'_1 \triangleright p'_2)$$

$$\parallel : \mathcal{P}_{CT}(\alpha, \beta) \times \mathcal{P}_{CT}(\gamma, \delta) \rightarrow \mathcal{P}_{CT}(\alpha \times \beta, \gamma \times \delta)$$

$$p_1 \parallel p_2 = (t, a) \mapsto (c, d, p'_1 \parallel p'_2)$$

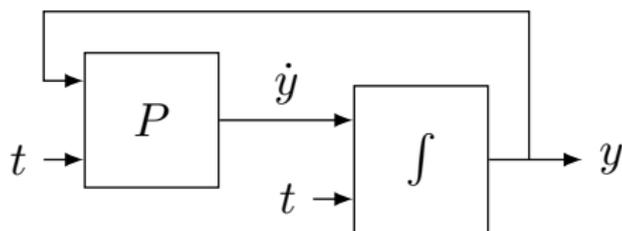
$$\star : \mathcal{P}_{CT}(\alpha, \beta) \times \mathcal{P}_{CT}(\alpha, \gamma) \rightarrow \mathcal{P}_{CT}(\alpha, \beta \times \gamma)$$

$$p_1 \star p_2 = (t, a) \mapsto (b, c, p'_1 \star p'_2)$$


 $p_1 \triangleright p_2$ 

 $p_1 \parallel p_2$ 

 $p_1 \star p_2$

$$\int : \mathcal{P}_{CT}(\alpha \times \beta, \gamma) \rightarrow \mathcal{P}_{CT}(\alpha, \gamma)$$

$$\int_{t_0, y_0} p_1 = (t, a) \mapsto (c, \int_{t, c} p'_1)$$



PROCESS WITH NO  
DATA INPUT  
(TOP LEVEL)

OBSERVATION/  
SYNCHRONIZATION  
TIME INSTANT.

$$at : \underbrace{P_{CT}(\perp, \alpha)}_{f(t)} \rightarrow \tau \rightarrow \alpha$$

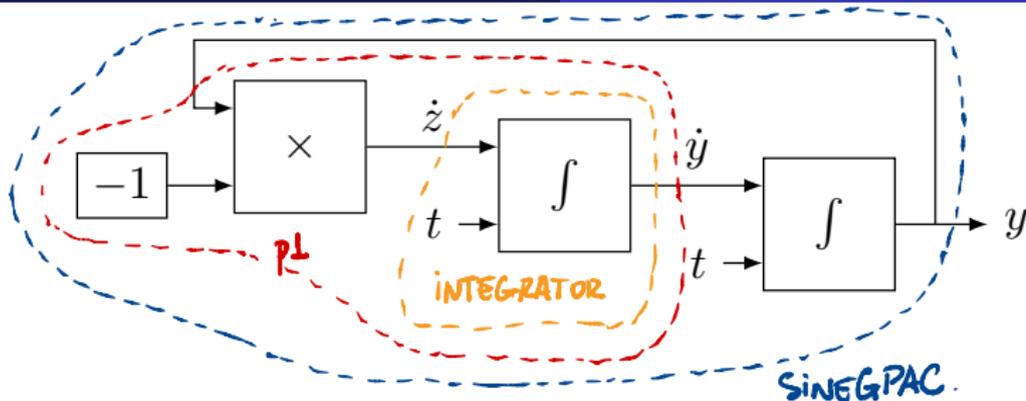
SYSTEM  
RESPONSE

`at :: PCT () a -> Time -> a`

`p 'at' t = pT`

where `(pT, _) = prCT p t ()`

"ADVANCE PROCESS *p* UNTIL TIME *t*."



```
sineGPAC = intCT rk4 0 0 p1
```

where

```
p1 = (constCT (-1) *** idCT) >>> multCT >>> integrator
```

```
integrator = intCT rk4 0 1 loopBreaker
```

```
loopBreaker = (idCT *** constCT 0) >>> adderCT
```

```
tScale k = (idCT &&& constCT k) >>> multCT
```

```
tShift k = (idCT &&& constCT k) >>> adderCT
```

```
p1 = time >>> sineGPAC
```

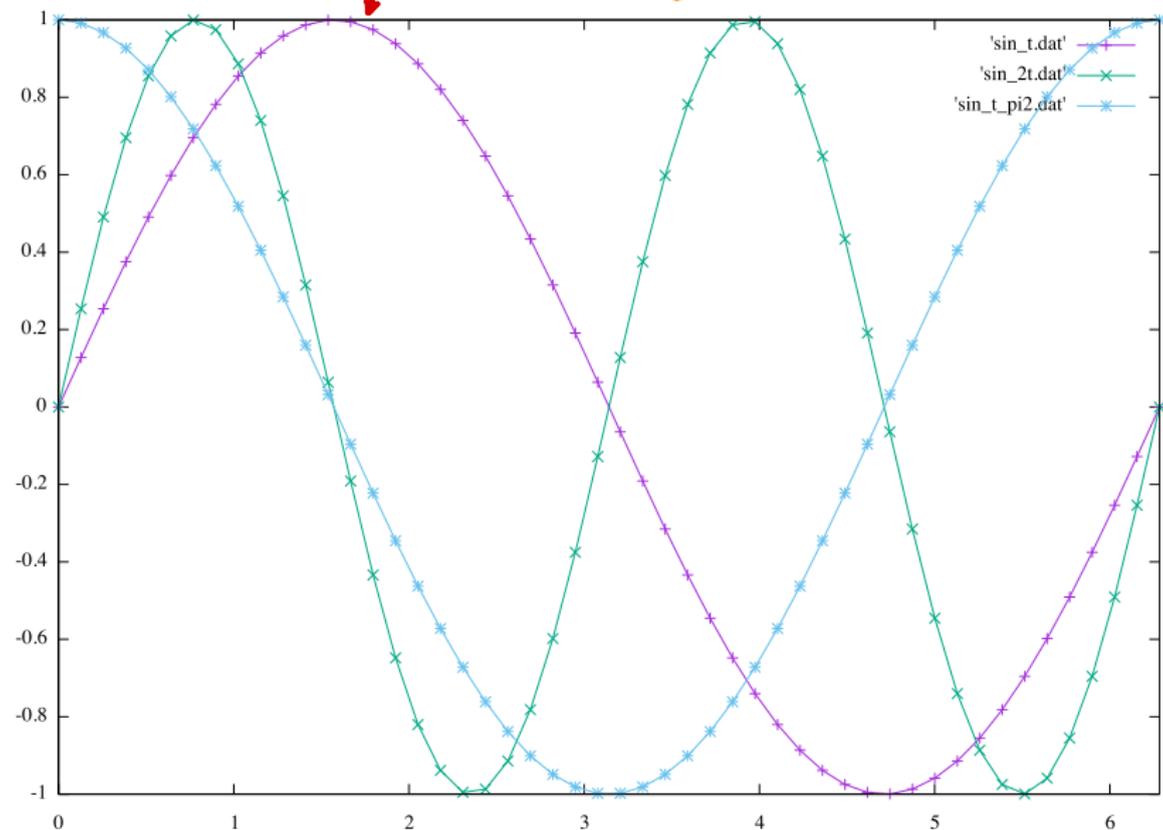
```
p2 = time >>> tScale 2 >>> sineGPAC
```

```
p3 = time >>> tShift (pi/2) >>> sineGPAC
```

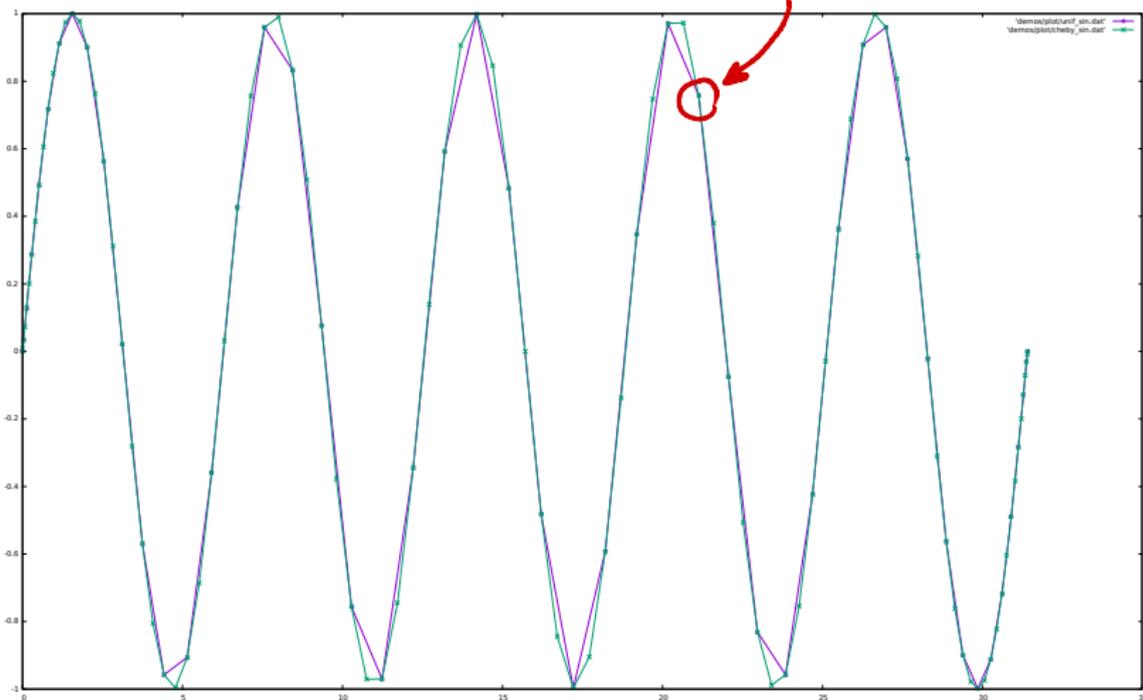
SAMPLER

TIME SCALING/SHIFTING IS NOT  
PART OF THE SYSTEM MODEL  
SEMANTICS.

SAME SYSTEM = DIFFERENT RESPONSES → TIMING SPECIFICATION SHALL BE EXPLICIT!



SYSTEM RESPONSE SHALL BE  
INDEPENDENT OF SAMPLING.



- a) Interaction with discrete-time MoCs.
- b) Semantic preserving transformations.
- c) Implementation in HW constrained platforms.



José E. G. de Medeiros, George Ungureanu, and Ingo Sander.  
An algebra for modeling continuous time systems.  
*In Design, Automation and Test in Europe (DATE)*. Dresden, 2018.



Ingo Sander, Axel Jantsch, and Seyed-Hosein Attarzadeh-Niaki.  
ForSyDe: System design using a functional language and models of computation.  
*In Handbook of Hardware/Software Codesign*. Springer, Dordrecht, 2017.

## More information on ForSyDe

<https://forsyde.github.io/>

 IT IS ALL OPEN-SOURCE!